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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/008,271	11/09/2001	James P. Freyensee	5181-96500	2600
7590	11/02/2006		EXAMINER PHAN, THAI Q	
Lawrence J. Merkel Meyertons, Hoods, Kivlin, Kowert & Goetzel, P.C. P.O. Box 398 Austin, TX 78767-0398			ART UNIT 2128	PAPER NUMBER

DATE MAILED: 11/02/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

DETAILED ACTION

This Office Action is in response to applicants' amendment filed on 08/15/2006.

Claims 1-39 are pending in the action.

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1-39 are rejected under 35 U.S.C. 102(e) as being anticipated by Bealkowski et al, US patent no. 6,282,596 B1.

As per claim 1, Bealkowski anticipates a method and system for simulating and supporting a hot plug or hot pull process or node to its network system with feature limitations very identical to the claimed invention. According to Beadles, the method includes steps

Configuring a first node, the node configured to participate in a simulation of a system under test, the configured node is to simulate the component under test (col. 5, lines 31-40)

A second node or processor configured to transmit a hot plug or hot pull command to the design (col. 5, lines 53-60),

Configuring a second network node to transmit a pull command designating to the first node (col. 6, lines 16-30), and

Responsive to the hot pull command, the first node simulates a removal or addition of the component from the system under test (col. 8, line 13 to col. 9, line 32, for exemplary).

As per claims 2-3, Bealkowski anticipates a plurality of network processors or nodes/components, and nodes (processors) configuration to simulate system under test, component to component connection, etc (cols. 8 and 9).

As per claim 4, Bealkowski anticipates processor for participating the simulation, freeing simulation resources, halting the plug or pull simulation, etc (col. 9, lines 32-51).

As per claim 5, Bealkowski anticipates a controller (70) is to coordinate and connect the processors for hot plug or hot pull simulation (col. 5, lines 30-40, col. 6, lines 6-15).

As per claims 6-10, Bealkowski anticipates a controller interconnected with the processors to simulate, terminate, and/or participate the pull or plug process (col. 5, lines 31-40, col. 6, lines 16-31).

As per claims 11 and 12, Bealkowski anticipates a method and system for simulating and supporting a hot plug or hot pull process or node to its network system with feature limitations very identical to the claimed invention. According to Beadles, the method includes steps

Configuring a first node, the node configured to participate in a simulation of a system under test, the configured node is to simulate the component under test (col. 5, lines 31-40)

A second node or processor configured to transmit a hot plug or hot pull command to the design (col. 5, lines 53-60),

Configuring a second network node to transmit a pull command designating to the first node (col. 6, lines 16-30), and

Responsive to the hot pull command, the first node simulates a removal or addition of the component from the system under test (col. 8, line 13 to col. 9, line 32, for exemplary).

As per claim 12, Bealkowski anticipates a controller interconnected with the processors to simulate, terminate, and/or participate the pull or plug process (col. 5, lines 31-40, col. 6, lines 16-31).

As per claims 13-19, Bealkowski anticipates a plurality of network processors or nodes/components, and nodes (processors) configuration to simulate system under test, component to component connection, etc (cols. 8 and 9). Bealkowski also anticipates a processor for participating the simulation, freeing simulation resources, halting or ceasing the plug or pull simulation, etc (col. 9, lines 32-51), and anticipates a controller (70) in the hot plug or pull simulation to coordinate and connect the processors for hot plug or hot pull simulation (col. 5, lines 30-40, col. 6, lines 6-15).

Claims 21-39 are directed to a computer program product and system to execute the program product for performing steps in claims 1-20 above. Similarly, claims 21-39 are also rejected under the same rationales as set forth.

Response to Arguments

Applicant's arguments with respect to amended claims 1-39 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

1. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

1. US patent no. 6,915,253, issued to Chapman, Barry, on July 2005
2. US patent no. 7,010,607, issued to Bunton, William, on Mar. 2006

2. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thai Phan whose telephone number is 571-272-3783.

The examiner can normally be reached on Monday-Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kamini Shah can be reached on 571-272-2279. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 2128

3. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Oct. 26, 2006

Thai Phan
THAI PHAN
PRIMARY EXAMINER
TECHNOLOGY CENTER 2100